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<u>L48</u>	l32 and l37	2	<u>L48</u>
<u>L47</u>	l32 and l35	0	<u>L47</u>
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<u>L35</u>	(713/375,401)[CCLS]	829	<u>L35</u>
<u>L34</u>	(712/2-300)[CCLS]	12305	<u>L34</u>
<u>L33</u>	(712/2-300)![CCLS]	12305	<u>L33</u>
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<u>L32</u>	(stipulat\$5 or schedul\$4 of DEFAULT) near12 (halt\$5 or stop\$5 or suspend\$5 or held or hold\$3 or disabl\$5) near15 pipelin\$4	54	<u>L32</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L31</u>	L30 and (allocat\$6 or assign\$7 or DEFAULT OR schedul\$4 OR FORC\$4)	1	<u>L31</u>
<u>L30</u>	6216232.pn.	1	<u>L30</u>
<u>L29</u>	L24 and (allocat\$6 or assign\$7 or DEFAULT OR schedul\$4 OR FORC\$4)	0	<u>L29</u>
<u>L28</u>	L24 and (assign\$7 or DEFAULT OR schedul\$4 OR FORC\$4)	0	<u>L28</u>
<u>L27</u>	L24 and (DEFAULT OR schedul\$4 OR FORC\$4)	0	<u>L27</u>
<u>L26</u>	L24 and schedul\$4	0	<u>L26</u>
<u>L25</u>	L24 and schedul\$	0	<u>L25</u>
<u>L24</u>	6609193.pn.	1	<u>L24</u>
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<u>L23</u>	L19 and 19	0	<u>L23</u>
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<u>L20</u>	L19 and 16	15	<u>L20</u>
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<u>L16</u>	13 and 18	6	<u>L16</u>
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<u>L11</u>	12 and 17	7	<u>L11</u>
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<u>L8</u>	(712/225,227,300)[CCLS]	1502	<u>L8</u>
<u>L7</u>	(712/4-22)[CCLS]	1719	<u>L7</u>
<u>L6</u>	(712/2-300)[CCLS]	12305	<u>L6</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
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IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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Manne, S.; Klauser, A.; Grunwald, D.;

[Computer Architecture, 1998. Proceedings. The 25th Annual International Symposium on](#)
27 June-1 July 1998 Page(s):132 - 141

Digital Object Identifier 10.1109/ISCA.1998.694769

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Dennis, J.B.; Gao, G.R.;

[Supercomputing '88. \[Vol. 1\]. Proceedings.](#)

14-18 Nov. 1988 Page(s):368 - 373

Digital Object Identifier 10.1109/SUPERC.1988.44674

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Weiss, S.;

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22-25 Jan. 1995 Page(s):14 - 21

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Obaidat, M.S.; Abu-Saymeh, D.S.;

[Applied Computing, 1991. \[Proceedings of the 1991 Symposium on](#)

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Gilbert, D.A.; Garside, J.D.;

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6. Balanced microprocessor design keeps performance peaked

Iacobovici, S.; Iacobelli, F.;

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17-19 May 1989 Page(s):371 - 375

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Oklobdzija, V.G.;

[ASIC, 2003. Proceedings, 5th International Conference on](#)

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8. Use-based register caching with decoupled indexing

Butts, J.A.; Sohi, G.S.;

[Computer Architecture, 2004. Proceedings, 31st Annual International Symposium on](#)

19-23 June 2004 Page(s):302 - 313

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